

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

PAUL E LUSE, ET AL.

Application No.: 10/090,944

Filed: March 4, 2002

For: **CONTROL PROCEDURE SELECTION**

Art Group: 2818

Examiner: Not yet assigned

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: August 20, 2004



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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Gayle M. Bekish

08-20-04

Date

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

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of

4

Complete if Known

Application Number

10/090.944

Filing Date

March 4, 2002

First Named Inventor

Paul E Luse

Art Unit

2818

Examiner Name

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| Not yet assigned |
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Attorney Docket Number

42390P13502

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner
Signature

| Date | Considered |
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| Application Number | 10/090,944 |
| Filing Date | March 4, 2002 |
| First Named Inventor | Paul E Luse |
| Art Unit | 2818 |
| Examiner Name | Not yet assigned |
| Attorney Docket Number | 42390P13502 |

NON PATENT LITERATURE DOCUMENTS

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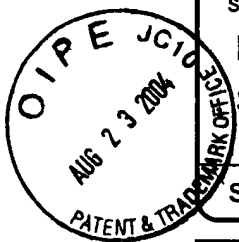
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet

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of

4

Application Number

10/090,944

Filing Date

3.4.02

First Named Inventor

Paul Luse

Art Unit

Examiner Name

Attorney Docket Number

42P13502

NON PATENT LITERATURE DOCUMENTS

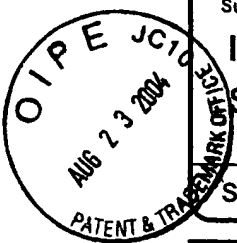
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|-----------------------|--------------------------|---|----------------|
| | 1 | Adaptec - Adaptec SCSI RAID 2000S Support | |
| | 2 | Adaptec - Adaptec SCSI RAID 2005S Support | |
| | 3 | Adaptec - Motherboard Partners and Compatibility | |
| | 4 | Adaptec - Adaptec SCSI RAID 2005S Technical Specs | |
| | 5 | Adpatec - Adaptec Article: Zero Channel RAID Frequently Asked Questions | |
| | 6 | Intel Corp., Product Brief, Intel Server Board SCB2 | |
| | 7 | Serial ATA: High Speed Serialized AT Attachment Revision 1.0, 29 August 2001 | |
| | 8 | Super0, Super P3TDDR, User's Manual, Revision 1.0a | |
| | 9 | PCI Local Bus, PCI BIOS Specification, Revision 2.1 August 26, 1994 | |
| | 10 | PCI Local Bus, PCT-to-PCI Bridge Architecture Specification, Revision 1.1, December 16, 1998 | |
| | 11 | PCI Local Bus, PCI Local Bus Specification, Revision 2.2, December 18, 1998 | |
| | 12 | PCI Local Bus, Mini PCI Specification, Revision 1.0, October 25, 1999 | |
| | 13 | PCI Local Bus, PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a, July 24, 2000 | |
| | 14 | PCI Local Bus, PCI Local Bus Specification, Revision 2.3, March 29, 2002 | |

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| | | First Named Inventor | Paul Luse |
| | | Art Unit | |
| | | Examiner Name | |
| | | Attorney Docket Number | 42P13502 |
| Sheet | 4 | of | 4 |

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|---------------------------------|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | 15 | PCI Local Bus, Appendix D, Class Codes | |
| | 16 | PCI Local Bus, Appendix H, Capability IDs, 0003.13 | |
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